

ABSTRACT

Sub
No

This invention concerns an unpredictable microprocessor or microcomputer including a processor (1), a first working memory (51), a main memory (6) containing an operating system, a main program (P1) and a secondary program (P2) characterized in that it also includes:

- n a second working memory (52);
- n switching means which, during the performance of the programs, makes it possible to switch use as working memory towards either of the two working memories (51, 52) while preserving the content thereof;
- n these switching means include at least one block of registers (54) to memorize the context for the run-through of the programs in the main memory and a switching circuit (53) for the validation of the working memories and the access registers (A1-A3) (D1-D3) associated with each memory (51, 52, 6) and controlled by said switching circuit (53).

Figure 1